

```

graph LR
    I2[I/O DEVICE 2] <--> C1[CPU 1]
    C1 <--> E4[EXT. MEMORY 4]
    C1 <--> M3[MAIN MEMORY 3]
  
```

The diagram illustrates a computer system architecture. It consists of four main components: an I/O DEVICE (labeled 2), a CPU (labeled 1), EXT. MEMORY (labeled 4), and MAIN MEMORY (labeled 3). The I/O DEVICE is connected to the CPU via a bidirectional arrow. The CPU is connected to both the EXT. MEMORY and the MAIN MEMORY via bidirectional arrows.

```

graph TD
    Start([SEARCH PROCESS]) --> B1
    B1([B1]) --> B2{IS THERE  
CORRESPONDING  
RECORD?}
    B2 -- YES --> B5[DATA  
DECOMPRESSION]
    B2 -- NO --> B3{SEARCH  
ALL RECORDS?}
    B3 -- YES --> B4[NOTICE FOR NO  
CORRESPONDING  
RECORD]
    B3 -- NO --> B1
    B5 --> End([END])
    B4 --> End

```

FIG.2

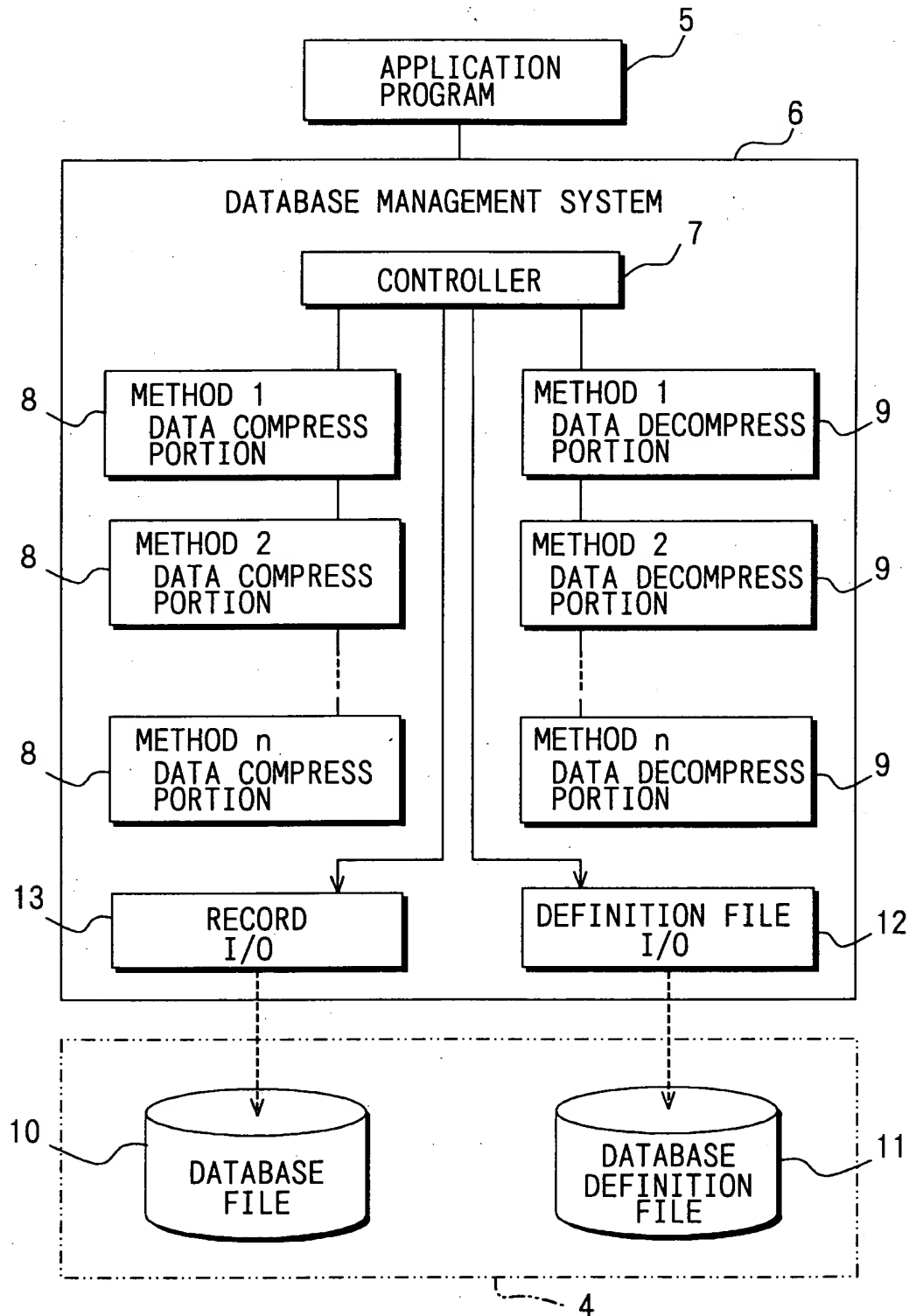


FIG.3

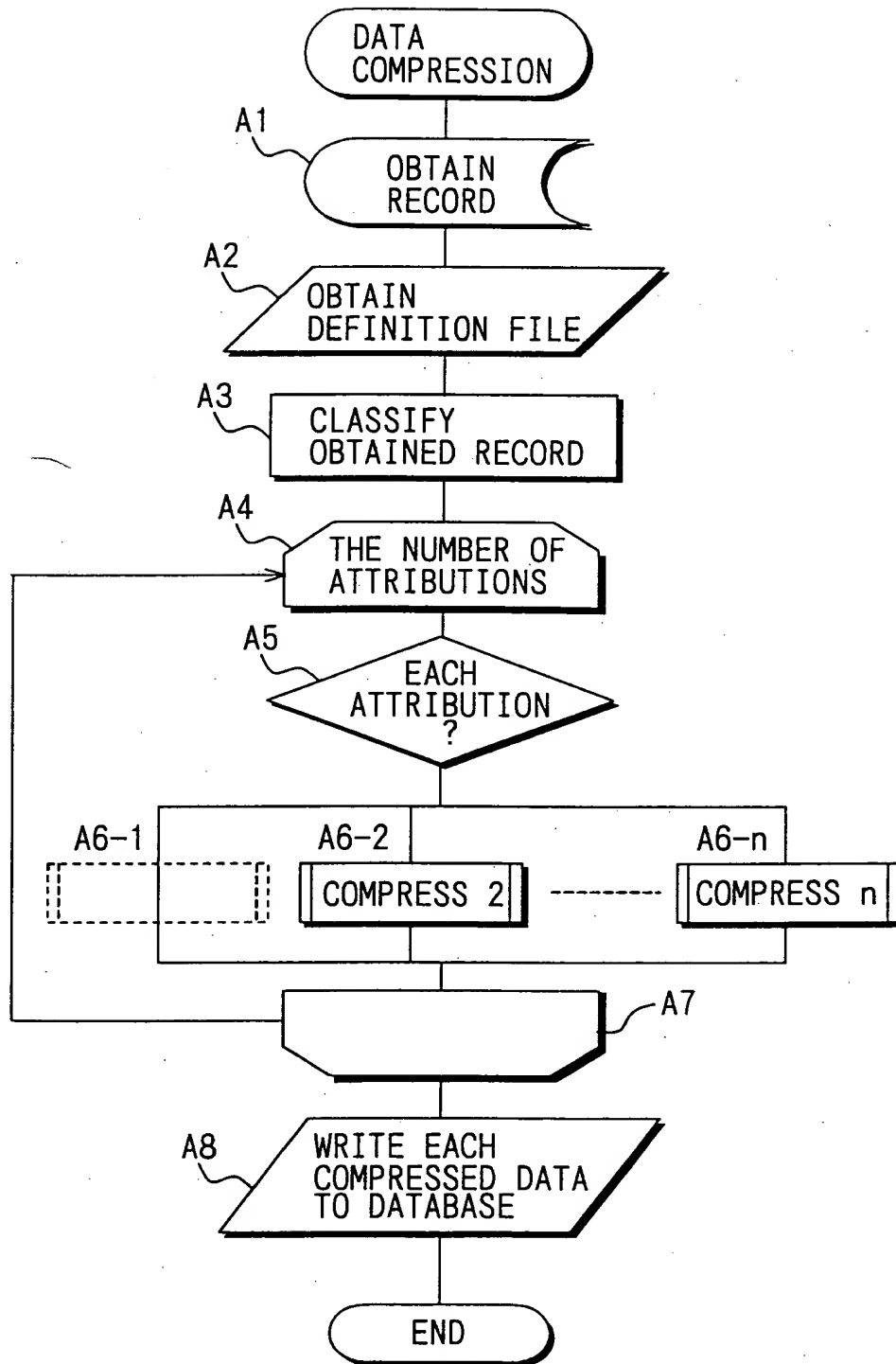


FIG.4A

A	B	C	D	E
100	211156985	TOKYOABC_COFFEE	03-3587-5412	MINATO-KU, TOKYO
201	211579865	KAGAWAGET_BUGG	54-8574	TAKAMATSU-SHI, KAGAWA-PREF.
205	211879654	NAKAMURAUMAE_Corp.	052-778-5241	NAGOYA-SHI, AICHI-PREF.
∴	∴	∴	∴	∴
864	211522365	CHUKYOUUMKCOPO_TV	075-845-6523	KYOTO-SHI, KYOTO-PREF.
1000	211987654	IBARAGISHELLGAS	0725-68-4587	IBARAGI-SHI, OSAKA-PREF.
1054	211453285	RUMOIROWSONSTORE	66-6842	RUMOI-SHI, HOKKAIDO
∴	∴	∴	∴	∴

DATA1

DATA2

DATA3

DATAm-2

DATAm-1

DATAm

FIG.4B

Ac	Bc	Cc	Dc	Ec
100	156985	04F5D234ED	7ED32D8	B674F7542DA532AD
201	579865	34986FED	98C5342	4632FD742D5768C0
205	879654	9804576	5789365	F358D438F385D581
∴	∴	∴	∴	∴
864	522365	745FED	8796845	8523DFA6426D032C
1000	987654	EA65	84E5A65	3896F4DE6140F34D
1054	453285	9462984D	8E68B68	542DA642032CF757
∴	∴	∴	∴	∴

DATA1

DATA2

DATA3

DATAm-2

DATAm-1

DATAm

FIG.6

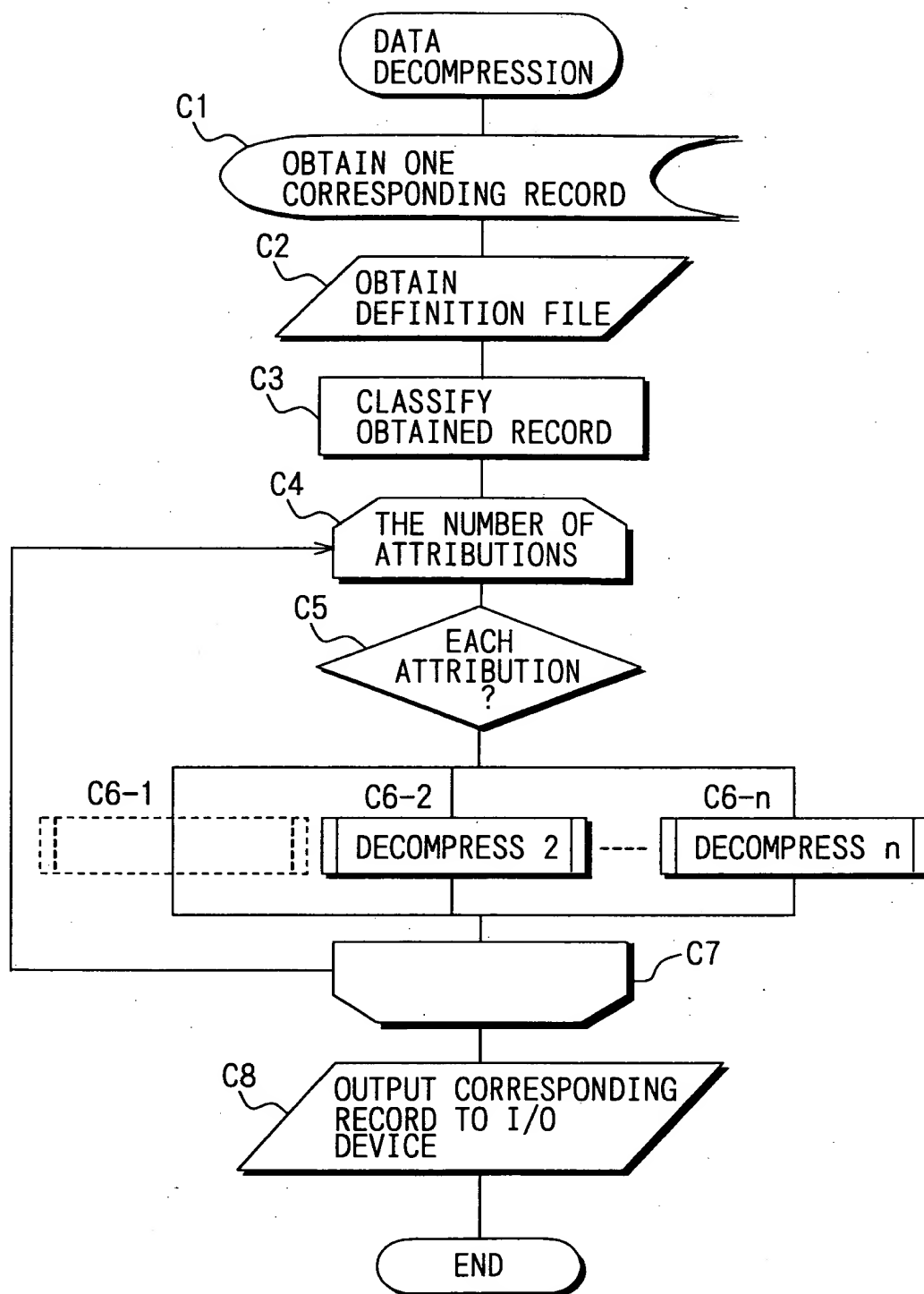


FIG.7

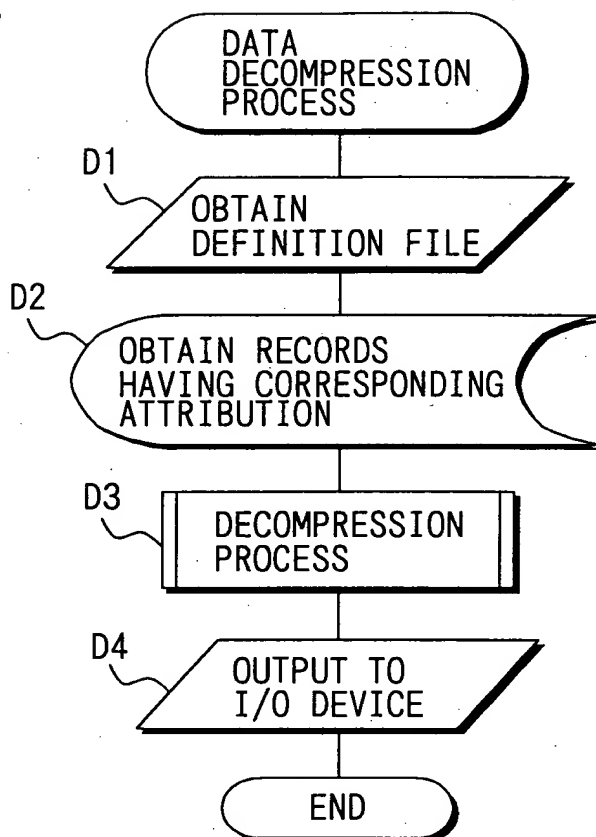


FIG.8

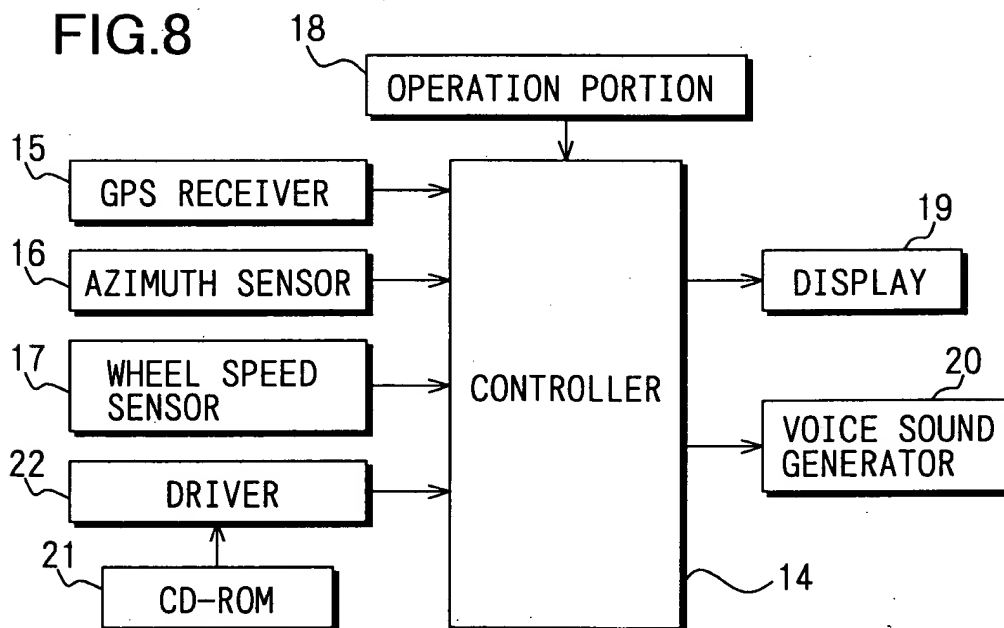
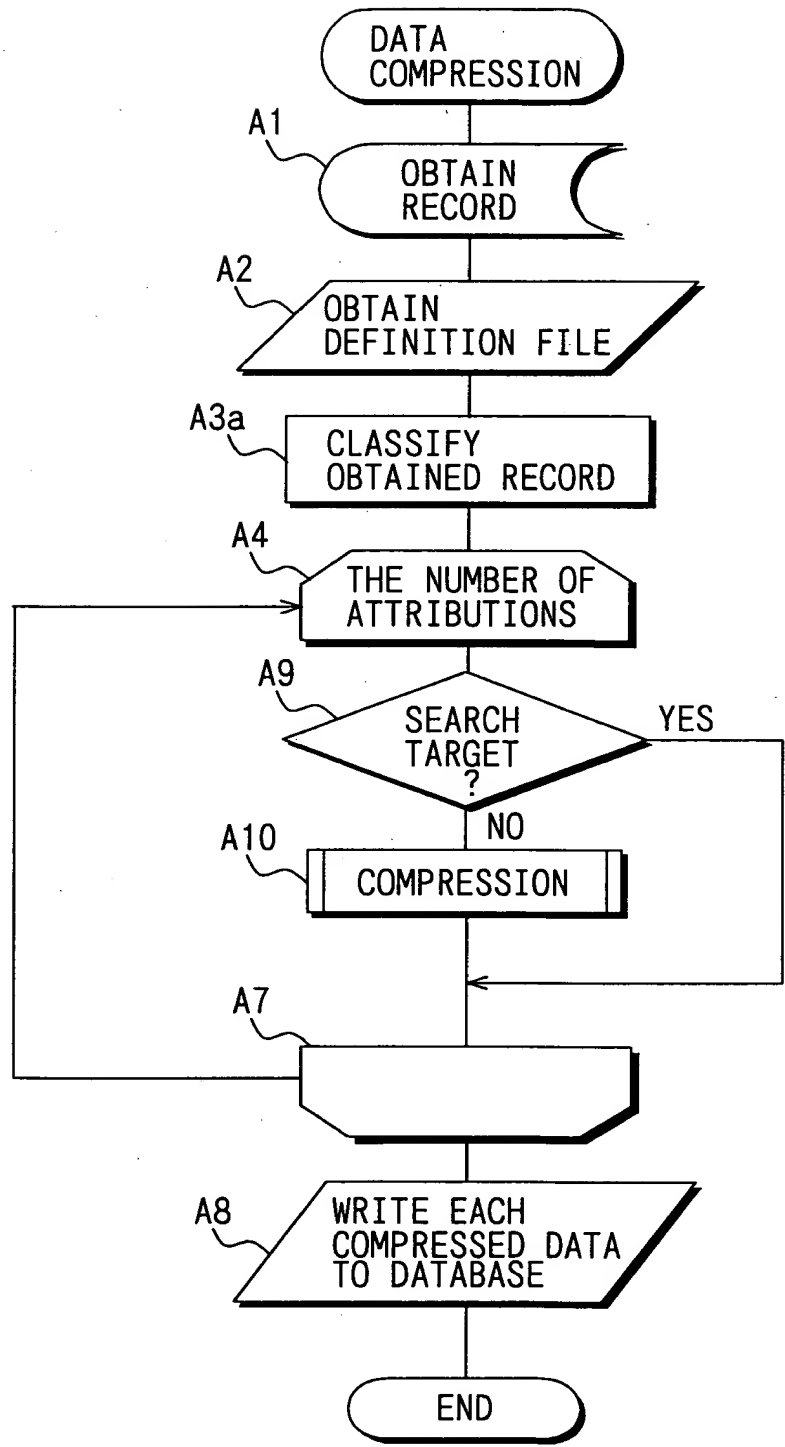


FIG.9



## DATA COMPRESSION

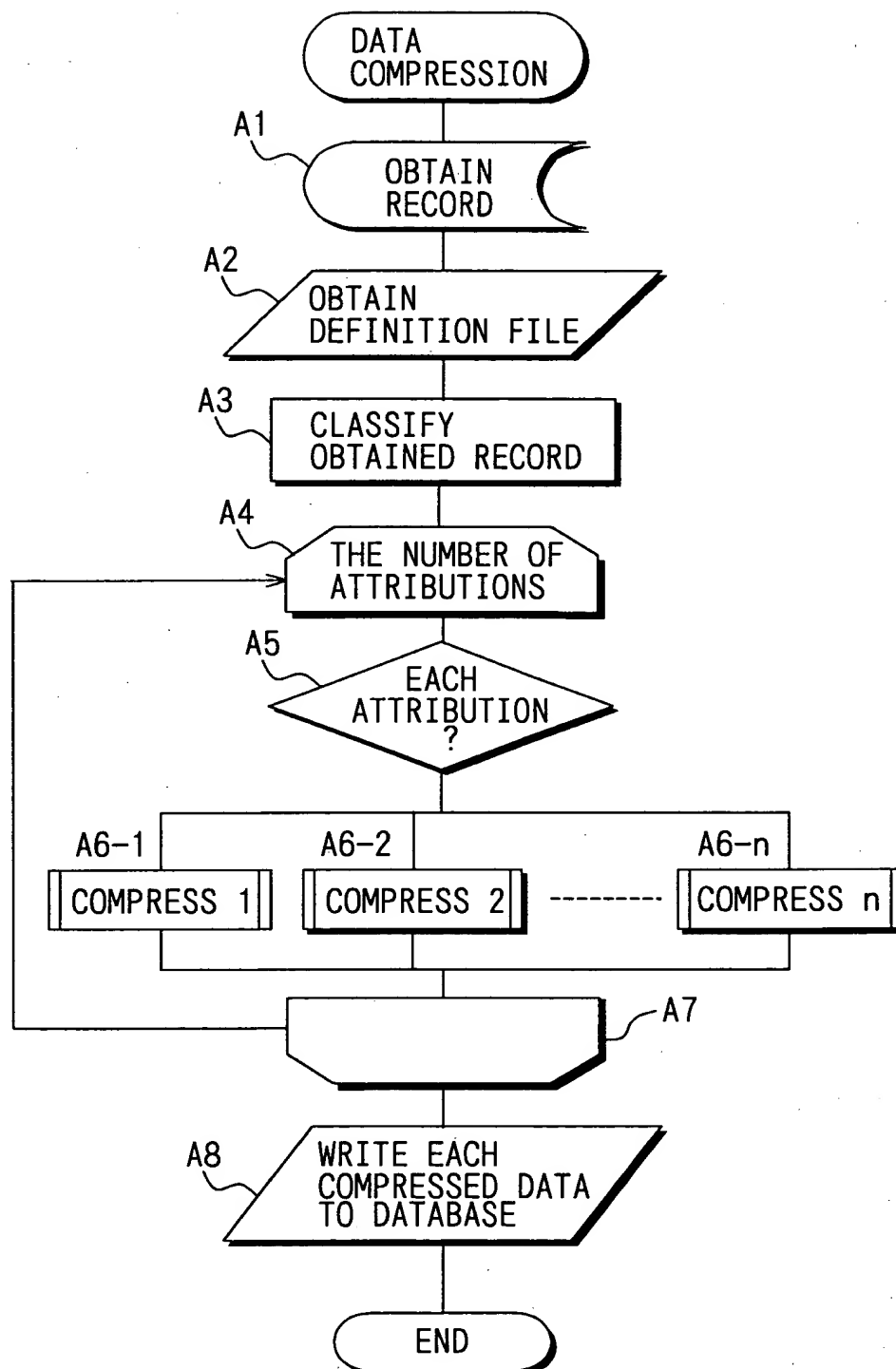




FIG.11

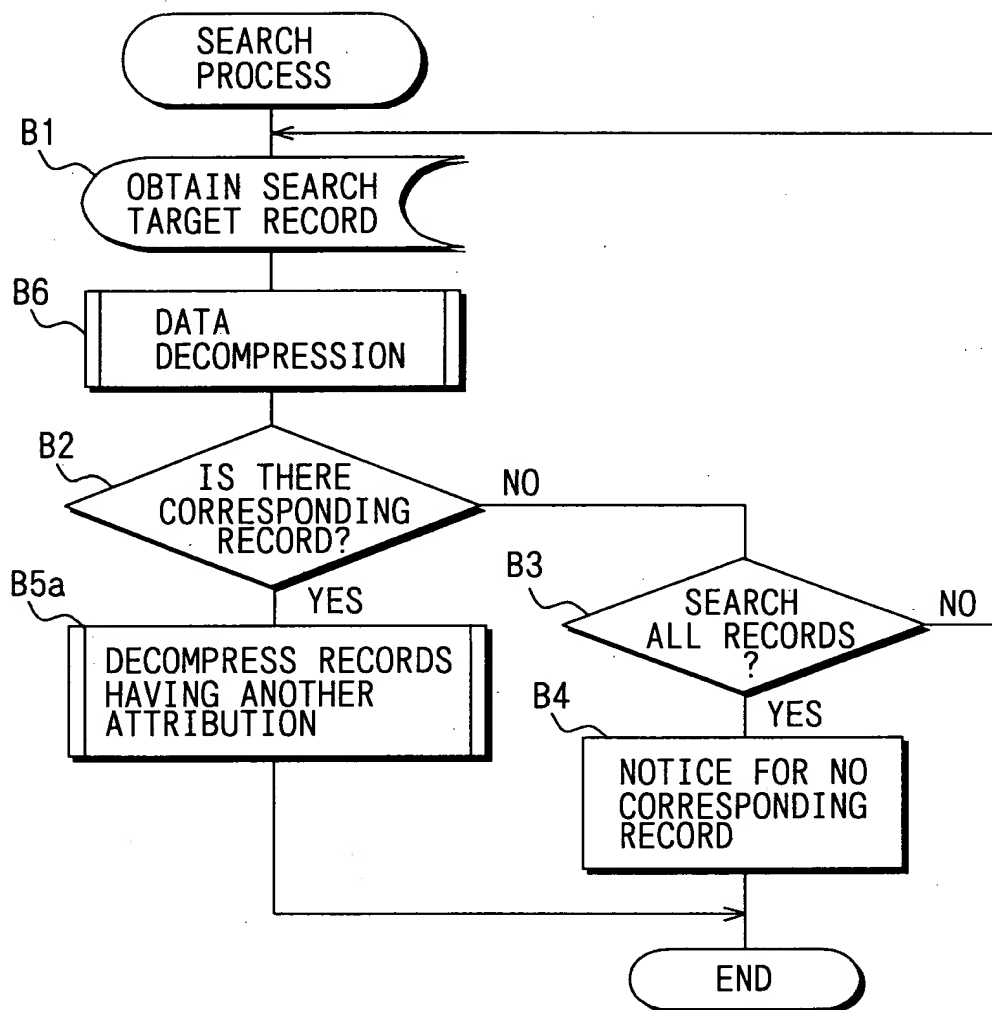


FIG.12

